



Docket No.: GR 95 2133 Re

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By: 

Date: July 15, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee : Holger Sedlak  
Applic. No. : 09/783,183  
Filed : February 14, 2001  
Title : Read-Only Memory Having Specially Output Circuits And Word Line Connected To A Group Of Memory Cells  
Art Unit: : 2824

TECHNOLOGY CENTER 2800

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

European Patent Application EP 0 627 742 A2 (Mochizuki et al.), dated December 7, 1994.

Respectfully submitted,



For Patentee

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U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEAttorney Docket No.:  
GR 95 P 2133 ReApplic. No.  
09/783,183

Patentee

Holger Sedlak

Filing Date  
February 14, 2001Group Art Unit  
2824INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT  
(37 CFR 1.98(b))

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

## FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J	0 627 742 A2	12/07/94	Europe			X
	K						
	L						
	M						
	N						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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	P	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.